

EPI's Third Generation MAJIC® Series Product Line-up

Introducing ... EPI's 3rd generation of **MAJIC® Series** intelligent JTAG debug probes, with EPI's unique **OpenDebug™** software support for world-class development environments.

Unlike probes that require use with proprietary debuggers, EPI provides the choice of both capabilities, EPI EDB and OpenDebug software to protect software investment.

OpenDebug™ Supported Tools

■ ARM	SDT, ADW, AXD
■ EPI	EDB
■ GNU	GDB
■ Green Hills	MULTI
■ IAR	C-SPY (EWARM)
■ Intel SDT	XDB
■ Mentor	code lab, XRAY
■ Metaware	SeeCode
■ Metrowerks	CodeWarrior
■ Microcross	Visual X-Tools
■ Microsoft	Platform Builder
■ MontaVista Linux	GDB
■ TimeSys Linux	GDB
■ µC Linux	GDB
■ Wind River	Tornado

New Product Lineup

MAJIC-LT - Targets the price sensitive buyer. Includes OpenDebug™ support for GDB.

MAJIC-LX - Targets single core SOC debugging. Includes EPI OpenDebug™ support for all OS environments and debuggers supported by EPI.

MAJIC-MT - Similar to LX, plus multi-session, multi-core concurrent debug support, and execution tracing support for processors with on-chip trace buffers (e.g. Intel XScale® microarchitecture).

MAJIC-PLUS - Full featured probe with all features of above probes, including multi-core support, PLUS 512K trace frame buffer to support ARM devices with Embedded Trace Macrocell (ETM) and MIPS EJTAG 2.0 devices with PCTrace.

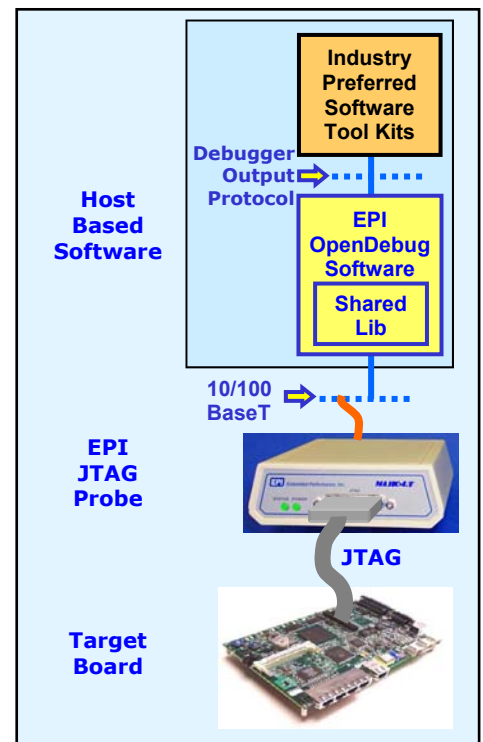


All MAJIC probes feature

- Non-intrusive debug (uses no target power, memory or I/O ports)
- Non-intrusive connect mode (target continues to run while connecting)
- Intrusive connect (target is stopped and reset at start of debug session)
- Interactive debug mode (debug steals cycles while target keeps running)
- Hardware breakpoint support (number determined by processor)
- Unlimited software breakpoints
- JTAG clock (see individual spec)
- Supports RT Clock mode for ARM

Included with all MAJIC probes

- Preconfigured startup files
- Preconfigured target files
- Flash programming utilities
- MAJIC Set-up Wizard (builds shortcut to select pre-built start-up files or create start-up files for your target configuration)
- EPI documentation on CDROM
- EPI Application Notes on CDROM
- Quickstart Guide
- Ethernet & serial cables
- Cable kit for specified target
- Power supply
- MONice Command Language Debugger
- GDB support



EPI Product Information & Support

EPI web site:

www.epitools.com

For EPI data sheets, application notes, specifications and cable kit information:

www.epitools.com/support/techlibrary.php

* EDB, EPI, MAJIC, OpenDebug and Virtual.One.Stop are trademarks or registered trademarks of Embedded Performance, Inc.

EPI
Embedded Performance, Inc.

Virtual.One.Stop™ Development Environments

Intel XScale® Processor Support

■ **Intel® Storage Components**

IOP310	80200
	80219
IOP321	80321
IOP331	80331

■ **Intel® Control Plane Processor**

IXC1100

■ **Intel® eXchange Architecture**

IXP420, 421, 422
IXP425
IXP2400
IXP2800, 2850

■ **Intel® Personal Internet Client Architecture (Intel PCA Processors)**

PXA25x, PXA26x & PXA27x

Standard JTAG Protocols/Cables

- Supports ARM standard CPU cores and architectural licensees (Intel XScale®, TI OMAP, Freescale DragonBall) processors (ARM 14, 20-pin)
- Supports MIPS standard CPU cores and architectural licensees (various cable configurations, incl. 14-pin)

[CK-ARM14](#) 14-pin ARM JTAG

[CK-ARM20](#) 20-pin ARM/Intel XScale

[CK-EJ12](#) 12-pin MIPS EJTAG 2.0

[CK-EJ20](#) 20-pin MIPS EJTAG 2.0

[CK-EJ28](#) 28-pin MIPS EJTAG 2.0

[CK-EJ38](#) 38-pin MIPS EJTAG 2.0

[CK-EJ52](#) 52-pin MIPS EJTAG 2.0

[CK-FPJ12](#) 12-pin ARM/Intel XScale

[CK-IDT24](#) IDT EJTAG 2.0

[CK-J10](#) 10-pin MIPS EJTAG 2.0

[CK-J12](#) 12-pin MIPS EJTAG 2.0

[CK-MIPS14](#) 14-pin MIPS EJTAG 2.5,2.6

[CK-TX14](#) 20-pin Texas Instruments

For MAJIC JTAG Interface Specifications, EPI documentation and Application Notes, consult:

www.epitools.com/support/techlibrary.php

Supported Hosts

- Windows: 98, NT, Me, 2000, XP (see individual product specifications)
- Linux/GDB supported on Linux PC and Solaris

ARM® Device Support

■ ARM cores	7TDMI, 7TS, 720T, 9TDMI, 9E-S, 920t, 922T, 926EJ-S, 940T, 946E-S, 966E-S
Altera	EXPA Series
Atmel	AT7x, AT91x
Cirrus	EP7x, EP9x
Conexant	CN/CXxxxxx
Faraday	FA526 core
Freescale	i.MX DragonBall
GlobespanVirata	Helium 100, 210-80, 500
LSI Logic	ARM cores
NetSilicon	NET+ARM NS7520 NS9750
Oki	ML67xxx
Phillips	ARM cores
Qualcomm	MSMxxxx
Samsung	S3xxxxx
Sharp	LH7xxxx
ST Micro.	ARM7/9 SoCs
Texas Inst.	ARM7 in DSPs ARM7/9 in OMAP
Triscend	TA7S20
Winbond	W90N740
Zeevo	TC2000 Series

MAJIC-LT Electrical Specs

JTAG Clock, LT:	0 or 10 MHz
LX, MT, PLUS	prog. 0 - 40 MHz
Target V., LT:	+3.3V
LX, MT, PLUS:	Autosense 1.2 - 3.3V
Serial Interface	RS232 to 115kbaud
Ethernet	10/100BaseT
LEDs	Status, Power, Run, Connect
Power Supply	+9V, 2A

External AC Adapter

Output, LT,LX,MT PLUS:	9 VDC, 2.0A 5 VDC, 4.0A
Input voltage	90 - 264 VAC
Input frequency	47 - 63 Hz
Input power	0.6 A
AC connector	EN 60320/13
DC conn. LT LX, MT, PLUS:	1.7 mm coaxial, 2.1 mm center positive, female

MIPS® Device Support

■ MIPS cores	MIPS32, MIPS64 4Kc, m, p 4KEc, m, p 5Kc, f Architectural license
Analog Devices	AD6xxx
ATI Technologies	Xilleon 22x
Brecis	MSPxxxx
Broadcom	BCM11xx BCM33xx BCM43xx BCM47xx BCM53xx BCM58xx BCM63xx BCM71xx BCM73xx
Cavium	CN2xx
IDT	79RC323xx 79RC324xx
Ishoni Networks	PD2xxx
Lexra	LX4/5xxx
LSI Logic	EZ4102/3
Micronas	MDE95xx
Philips	PR19xx/3940
PMC-Sierra	MSPxxxx
Texas Inst.	TNETx-xxxx

Flash Programming Utility

Flash programming utilities are provided for a wide range of standard device type from:

AMD	Intel
Atmel	Fujitsu,
Micronix,	ST Microelectronics
SST	
Toshiba	

Ask for current list of supported device types or support assistance for your specific device.



Embedded Performance, Inc.
 606 Valley Way
 Milpitas, CA 95035
 Phone: 408-957-0350
 FAX: 408-957-0307
 Sales: sales@epitools.com
 URL: www.epitools.com